

Notice of References Cited	Application/Control No. 09/667,706	Applicant(s)/Patent Under Reexamination INOUE ET AL.	
	Examiner Douglas W Owens	Art Unit 2811	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-2002/0004303	01-2002	Agnello et al.	438/689
	B	US-6,436,753	08-2002	Ikeda et al.	438/238
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	S. Wolf. et al. SILICON PROCESSING FOR THE VLSI ERA. 1986. Lattice Press. Vol. 1., pages 308 and 309.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.